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APPLICANTS
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**** CONTINUING DATA *******
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**** FOREIGN APPLICATIONS *******
 JAPAN 2003-017612 01/27/2003

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Foreign Priority claimed 35 USC 119(a-d) conditions met Verified and Acknowledged	<input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <input checked="" type="checkbox"/> Yes <input type="checkbox"/> No <u>/JOSEPH D. TORRES/</u> Examiner's Signature	<input checked="" type="checkbox"/> Met after Allowance <u>/JDT/</u> Initials	STATE OR COUNTRY JAPAN	SHEETS DRAWINGS 18	TOTAL CLAIMS 14	INDEPENDENT CLAIMS 7
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TITLE
 Calculation processing device, calculation processing device design method, and logic circuit design method

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